Docket #: P/1071-1598
Title: MOUNTING BOARD AND ELECTRONIC DEVICE USING THE SAME
Inventor: Hiroyuki ISHIZAKI
Contact: James A. Finder (212) 382-0700
Drawings: 9 Sheets (Figs. 1-9)

FIG. 1

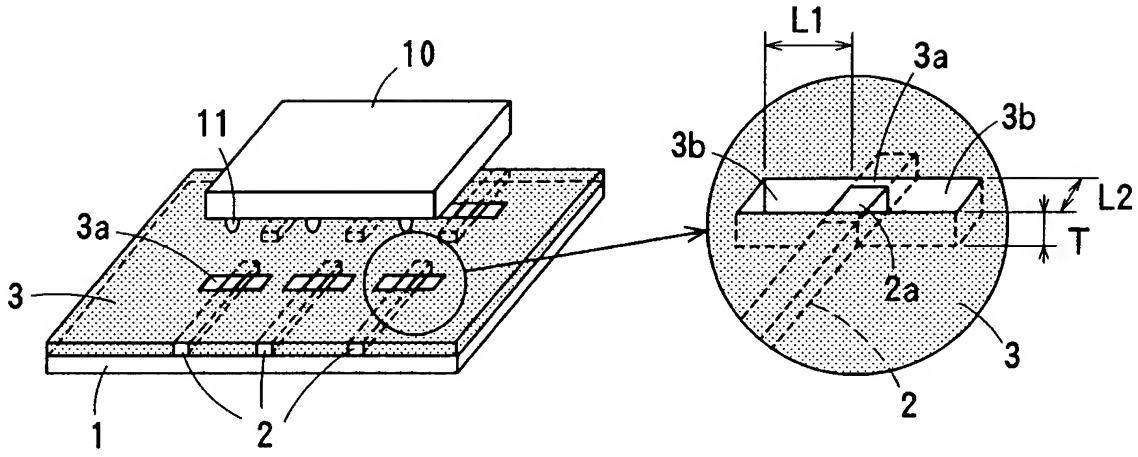

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FIG. 2

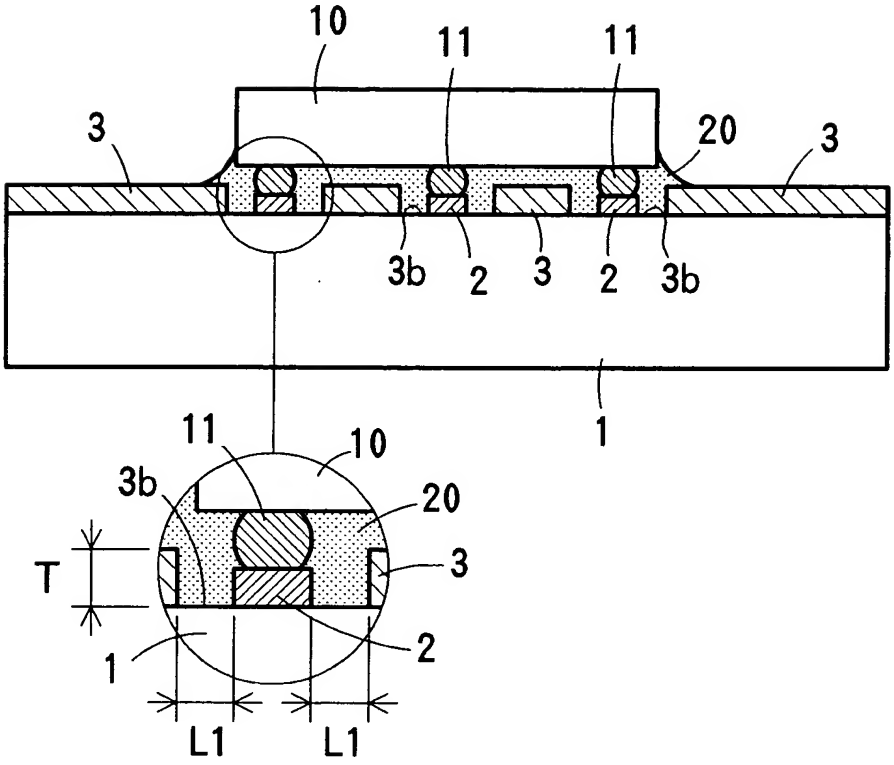

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FIG. 3A

STATE OF OCCURRENCE OF VOIDS (VISCOSITY: 0.4 Pa•s)

LONGITUDINAL LENGTH (L1) (L1)	INSULATOR THICKNESS (T) (T)	L1/T	OCCURRENCE OF VOIDS
30 μm	30 μm	1.00	VOIDS OCCURRED
50 μm	30 μm	1.67	VOIDS OCCURRED
60 μm	30 μm	2.00	NO VOID
70 μm	30 μm	2.33	NO VOID
90 μm	30 μm	3.00	NO VOID
30 μm	50 μm	0.60	VOIDS OCCURRED
50 μm	50 μm	1.00	VOIDS OCCURRED
70 μm	50 μm	1.40	VOIDS OCCURRED
90 μm	50 μm	1.80	VOIDS OCCURRED
100 μm	50 μm	2.00	NO VOID
120 μm	50 μm	2.40	NO VOID
150 μm	50 μm	3.00	NO VOID

STATE OF OCCURRENCE OF VOIDS (VISCOSITY: 7.3 Pa•s)

FIG. 3B

LONGITUDINAL LENGTH (L1) (L1)	INSULATOR THICKNESS (T) (T)	L1/T	OCCURRENCE OF VOIDS
30 μm	30 μm	1.00	VOIDS OCCURRED
50 μm	30 μm	1.67	VOIDS OCCURRED
60 μm	30 μm	2.00	NO VOID
70 μm	30 μm	2.33	NO VOID
90 μm	30 μm	3.00	NO VOID

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FIG. 4

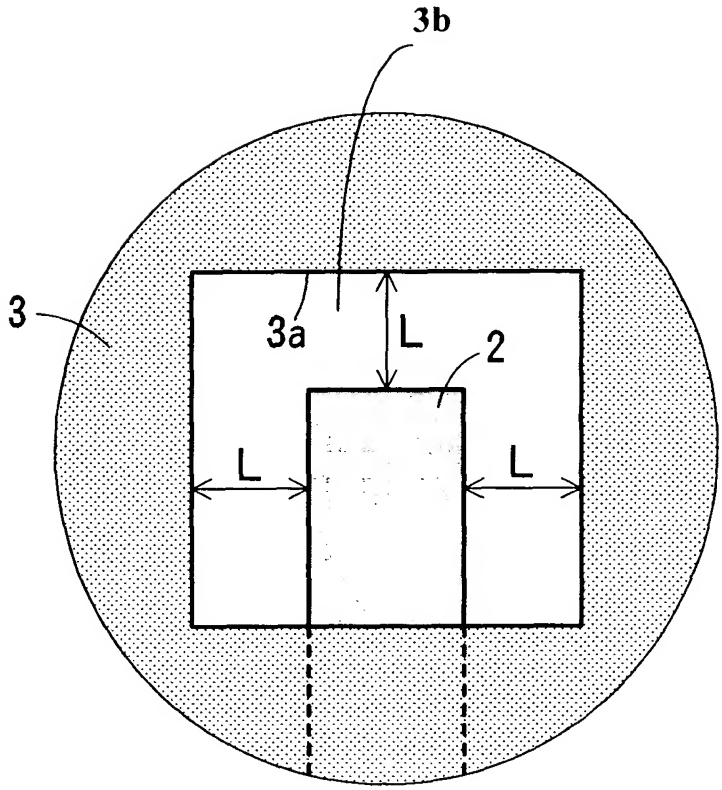

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FIG. 5

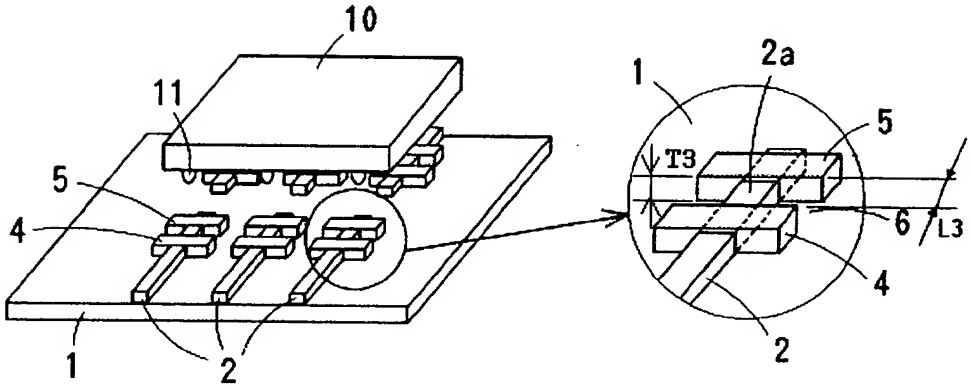

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FIG. 6

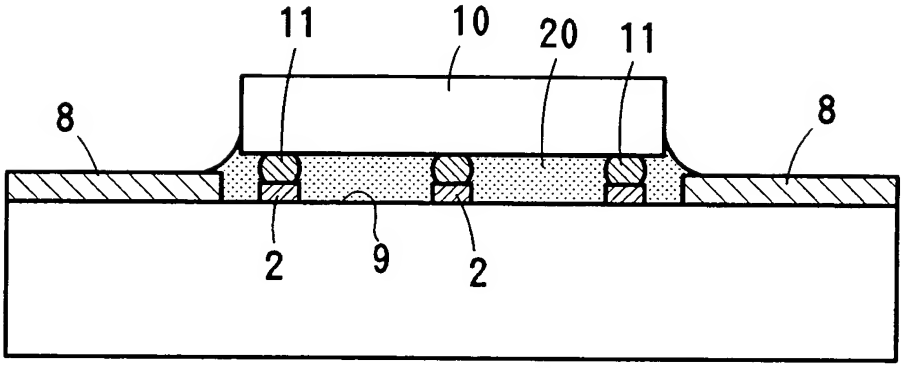

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FIG. 7

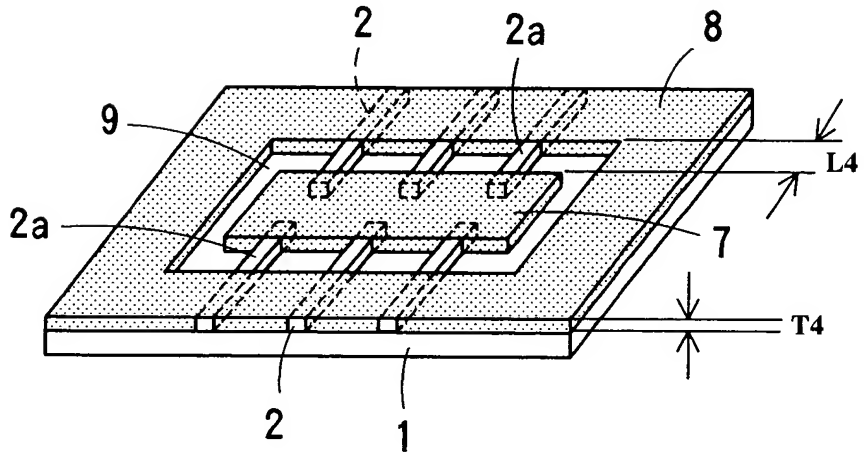

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FIG. 8 PRIOR ART

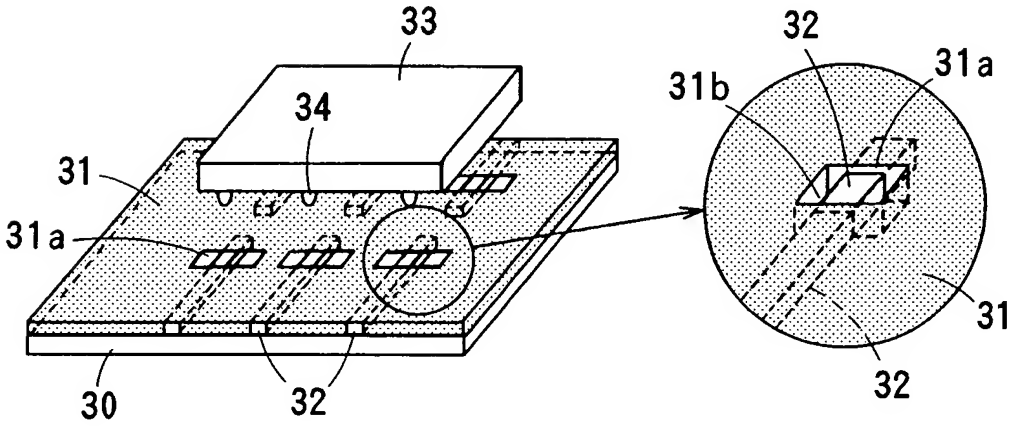

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FIG. 9 PRIOR ART